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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of

2

Complete if Known

Application Number	10/684.657
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Filing Date	10/15/2003
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First Named Inventor	ELLIS E FRAMPTON
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Art Unit	2822
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Examiner Name _____

Attorney Docket Number	081498-0306614
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
MP	A	DE	40 08 335 A1	09-26-1991	VIELHABEN		

**Examiner
Signature**

Date	
Considered	

9/05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known			
		Application Number	10/684,657		
		Filing Date	10/15/2003		
		First Named Inventor	FRAMPTON E ELLIS		
		Art Unit	2822		
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	081498-0306614

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
MP	A	NONE, "PC vision: Intel unveils plans to bring PCs to vehicles", EDGE: Work Group Computing Report, EDGE Publishing, p. 1-2, (October 28, 1996).	
MP	B	LI, YAO, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation," LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, (November 16-19, 1992).	
MP	C	DICKINSON ET AL., "An Integrated Free Space Optical Bus," 1989 IEEE International Conference on Computer Design" VLSI In Computers and Processors, p. 62-65, (October 2-4, 1989).	
MP	D	NATARAJAN ET AL, "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor," Journal of Lightwave Technology, Vol. 13 (No. 6), p. 1031-1040, (June 6, 1995).	
MP	E	ZHAO ET AL, "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems," Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, (October 23-24, 1995).	
MP	F	WU ET AL, "Microprocessor Control Signal Transmission through Optical Fiber," Conference Record of 1992 IEEE Industry Applications Society Annual Meeting, p. 1747-1750, (October 4-9, 1992).	

Examiner Signature		Date Considered	9/05
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